**A HIGH-PERFORMANCE VOLTAGE SAG DETECTION ALGORITHM FOR A DYNAMIC VOLTAGE RESTORER (DVR)**

***Report submitted to***

***V.R. Siddhartha Engineering College***

***for the award of the degree***

***Of***

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***In***

***Electrical and Electronics Engineering***

***by***

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**(Affiliated to JNTUK, Approved by UGC)**

**MARCH 2018**

**CERTIFICATE**

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# *This is to certify that, this Term paper report entitled “A HIGH-PERFORMANCE VOLTAGE SAG DETECTION ALGORITHM FOR A DYNAMIC VOLTAGE RESTORER (DVR) ” is the bonafide record of work carried out by P.V.M.THUSHARA (148W1A02A5), D.ASHA JYOTHI (158W5A0217),P.ANIL (148W1A02A3), K.VAMSI (148W1A0288), under my guidance and submitted on partial fulfillment of the requirements for the award of the degree of bachelor of technology in electrical and electronics engineering to Jawaharlal Nehru technological University, Kakinada during the academic year 2017 - 2018.*

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